



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/287,248

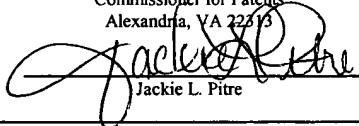
§ Examiner: K. Padmanabhan
§ Art Unit: 1641
§ Atty. Dkt. No.: 5119-00501

Confirmation No.: 6075

Filing Date: April 7, 1999

Inventors: McDevitt et al.

Title: FLUID BASED ANALYSIS
OF MULTIPLE ANALYTES
BY A SENSOR ARRAY

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8	
DATE OF DEPOSIT:	9/21/04
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail on the date indicated above and is addressed to: Commissioner for Patents Alexandria, VA 22313	
 Jackie L. Pitre	

PAYMENT OF ISSUE FEE

MAIL STOP: ISSUE FEE

Commissioner for Patents
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Alexandria, VA 22313-1450

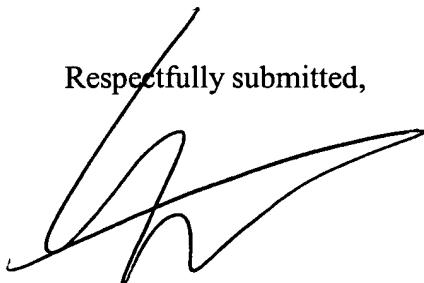
Sir:

Please find enclosed:

- 1) Issue Fee Transmittal Form PTOL-85B (1 page);
- 2) A fee authorization in the amount of \$680.00 (2 pages);
- 3) Comments on Statement of Reasons for Allowance (2 pages); and
- 4) A return postcard evidencing receipt of these materials.

If any fees are inadvertently omitted or if any additional fees are required or have been overpaid, please appropriately charge or credit those fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account Number 50-1505/5119-00501/EBM.

Respectfully submitted,



Eric B. Meyertons
Reg. No. 34,876

Attorney for Applicant

MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.
P.O. BOX 398
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(512) 853-8800 (voice)
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Date: 9/21/04

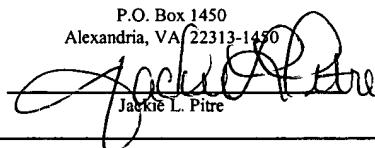


PATENT
5119-00501

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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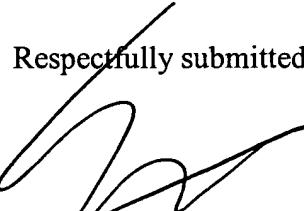
Applicant respectfully disagrees with the Examiner's Statement of Reasons for Allowance and/or Applicant believes that the Examiner's Statement of Reasons for Allowance is incomplete.

Applicant respectfully submits that the cited art does not teach or suggest the combinations of features set forth in the Applicant's claims. By way of nonlimiting example, Applicant submits the submitted references at least do not appear to teach or suggest a system that includes a sensor array with a cover positioned at a fixed distance above the surface of the sensor array.

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